



EV182663372

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/921,518  
Filing Date August 1, 2001  
Inventor Scott T. Moore  
Assignee Micron Technology, Inc.  
Group Art Unit 2818  
Examiner Phuc T. Dang  
Attorney's Docket No. MI22-1669  
Title: Method of Forming Integrated Circuitry, Method of Forming Memory Circuitry, and Method of Forming Non-Volatile Random Access Memory Circuitry

**RESPONSE TO SEPTEMBER 27, 2002 NOTICE OF ALLOWANCE**  
**PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING**

To: Box RCE  
Assistant Commissioner for Patents  
Washington, D.C. 20231

**VIA U.S. EXPRESS MAIL**

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated September 27, 2002,  
Applicant remarks as follows:

**REMARKS**

Claims 1-88 are in the application for consideration.

This Request for Continued Examination is submitted in an abundance of caution simply to place certain references before the Examiner for consideration. The references are referred to in the Supplemental Information Disclosure Statement presented herewith.

This application is believed to be in immediate condition for allowance, and action to that end is requested.

Respectfully submitted,

Dated: 10-9-02

By: 

Mark S. Matkin, Reg. No. 32,268